

FIG. 1

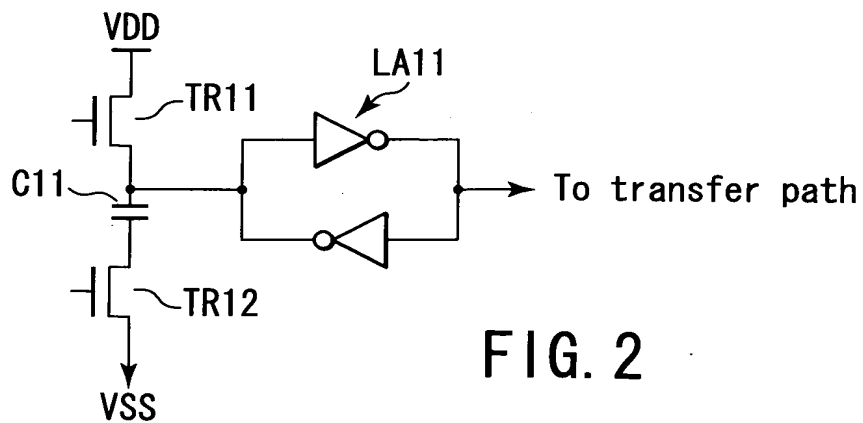


FIG. 2

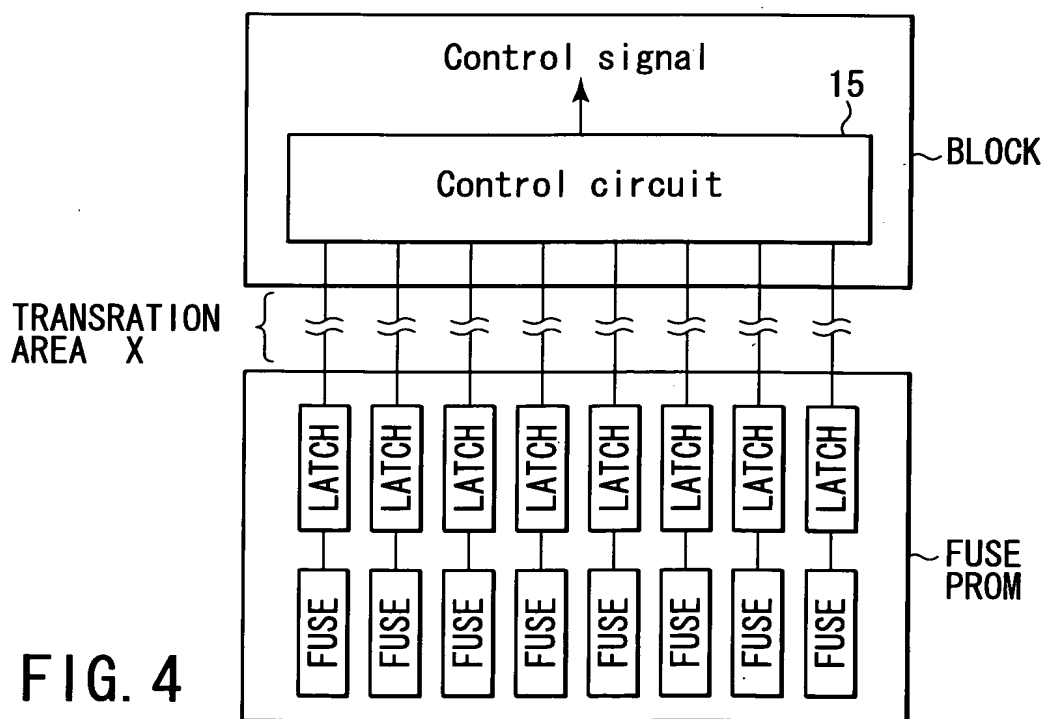
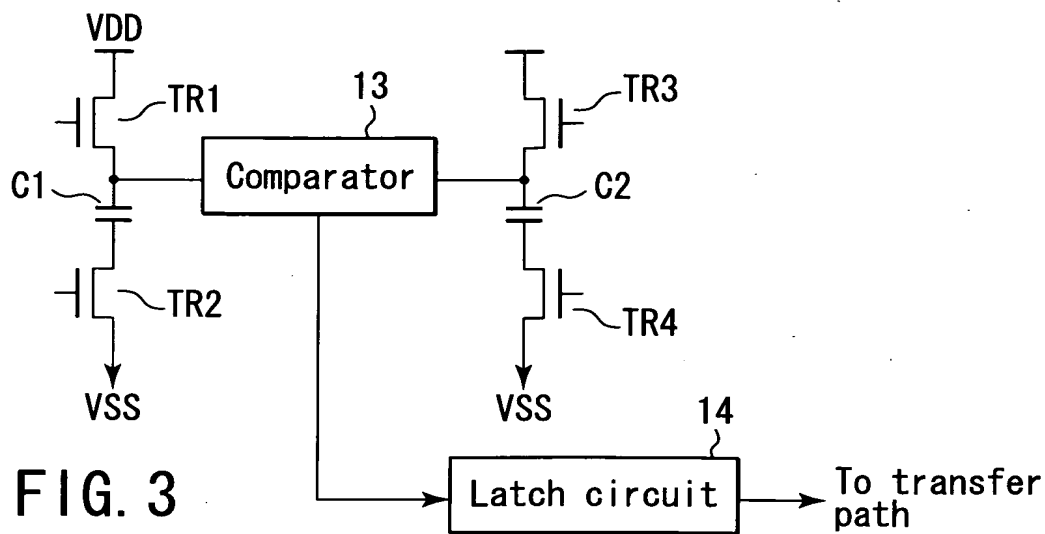
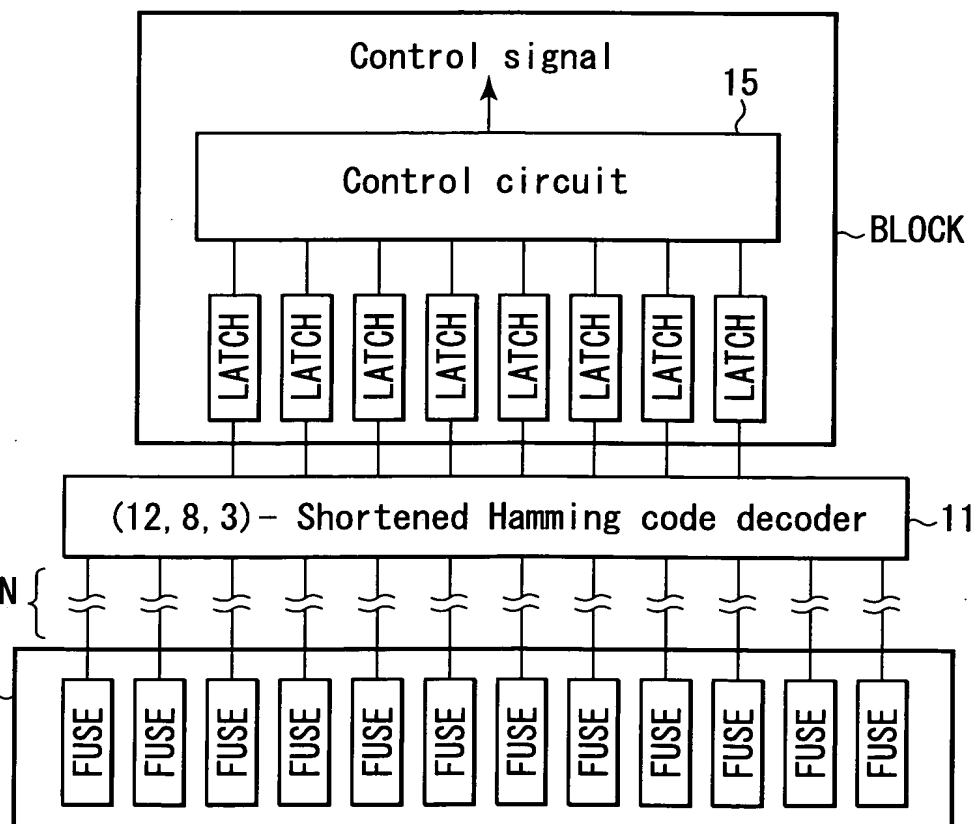
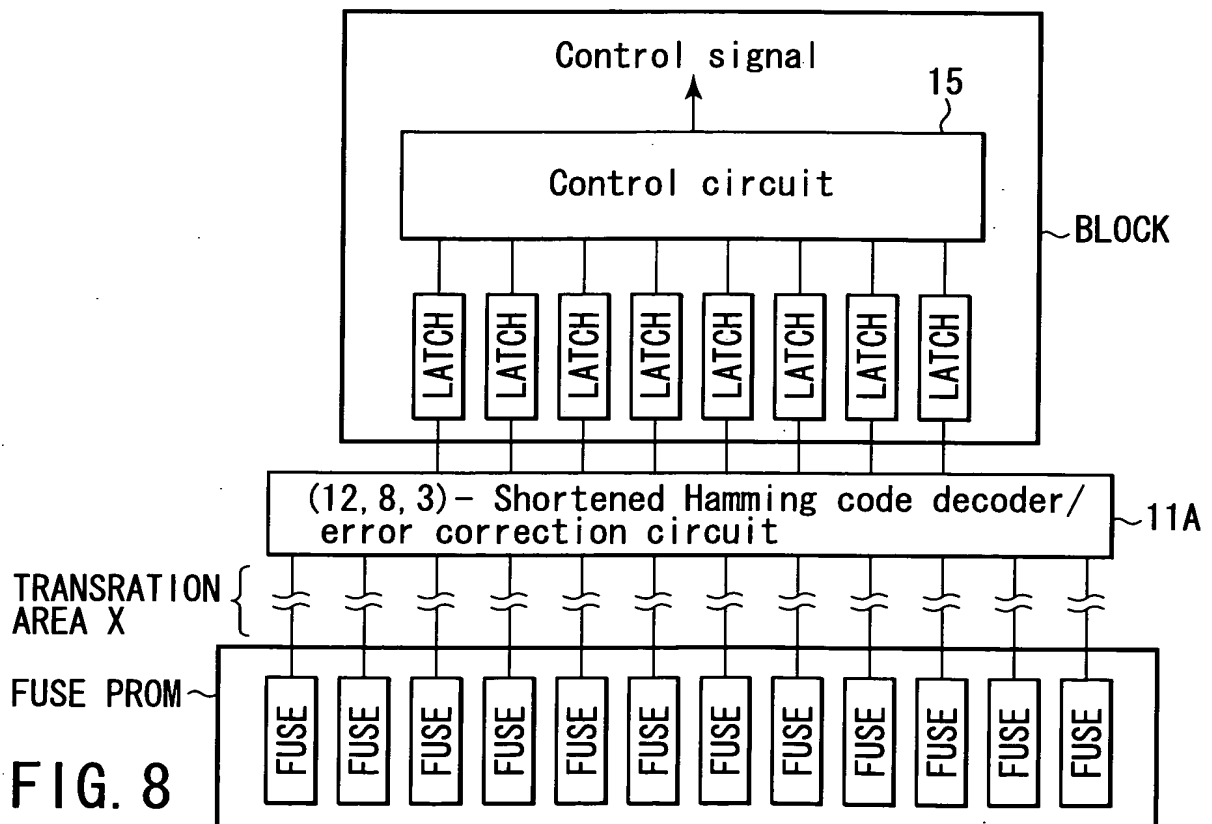
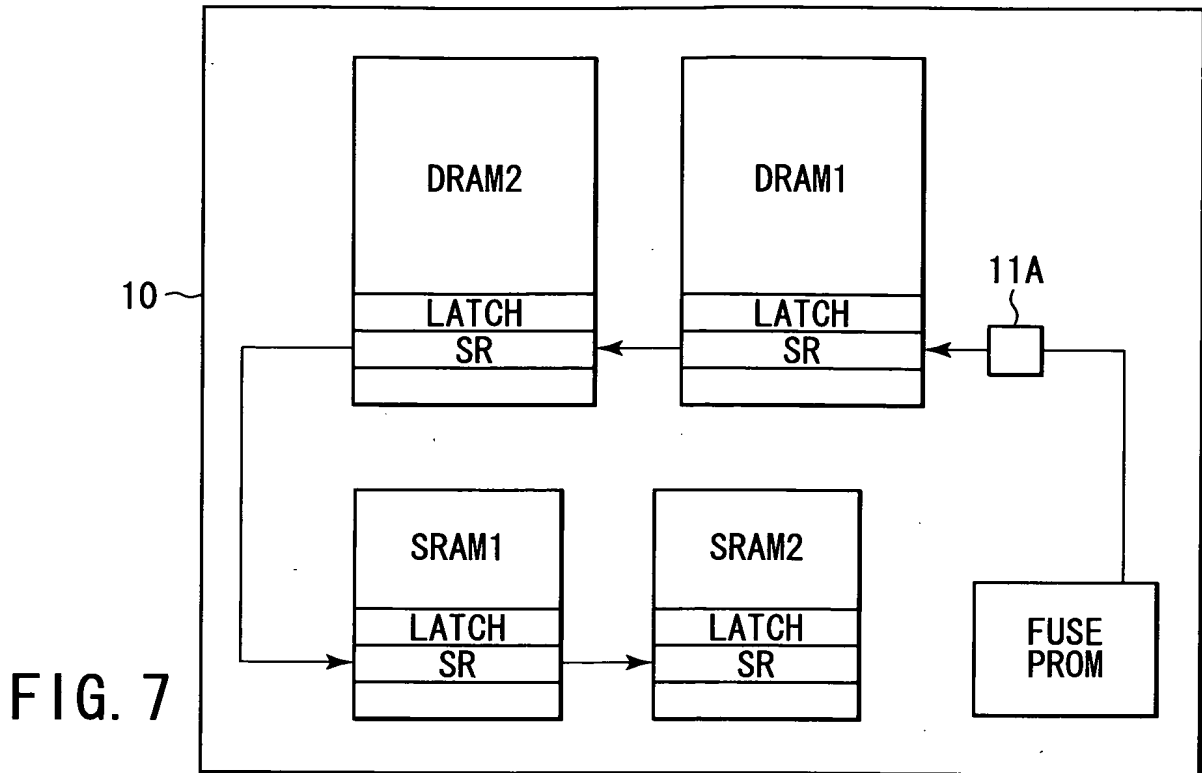
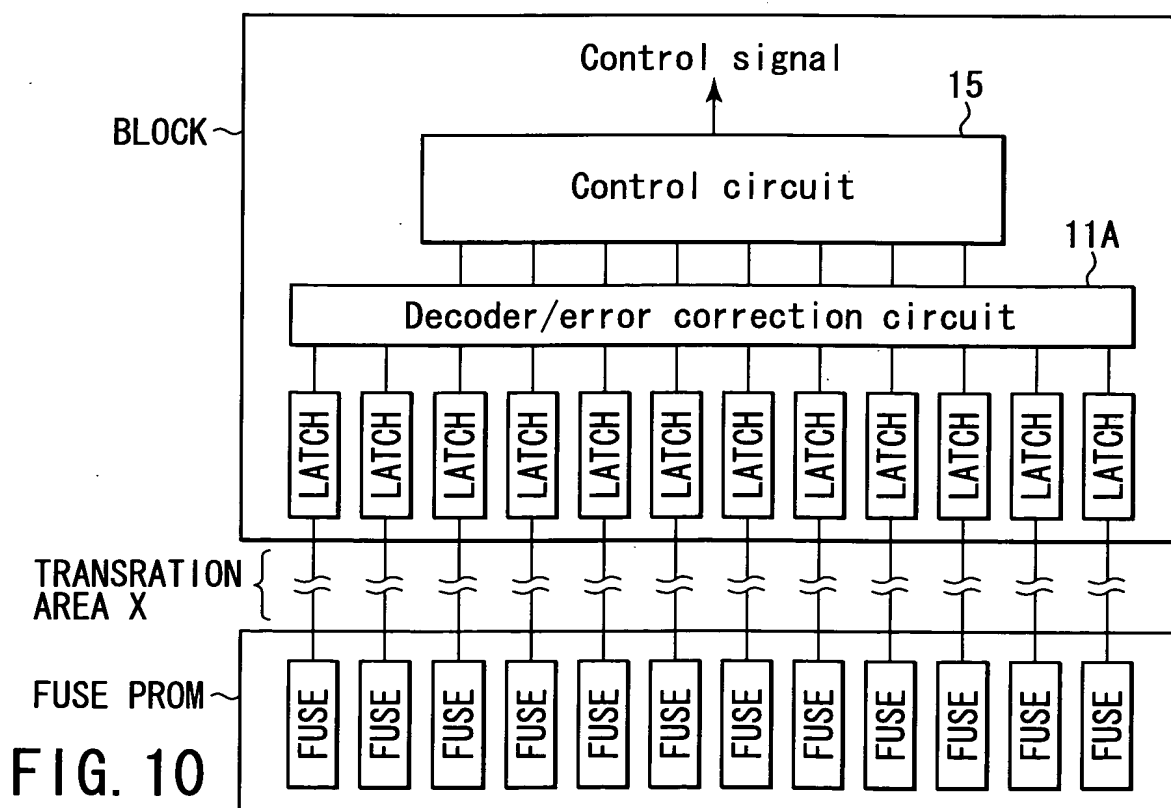
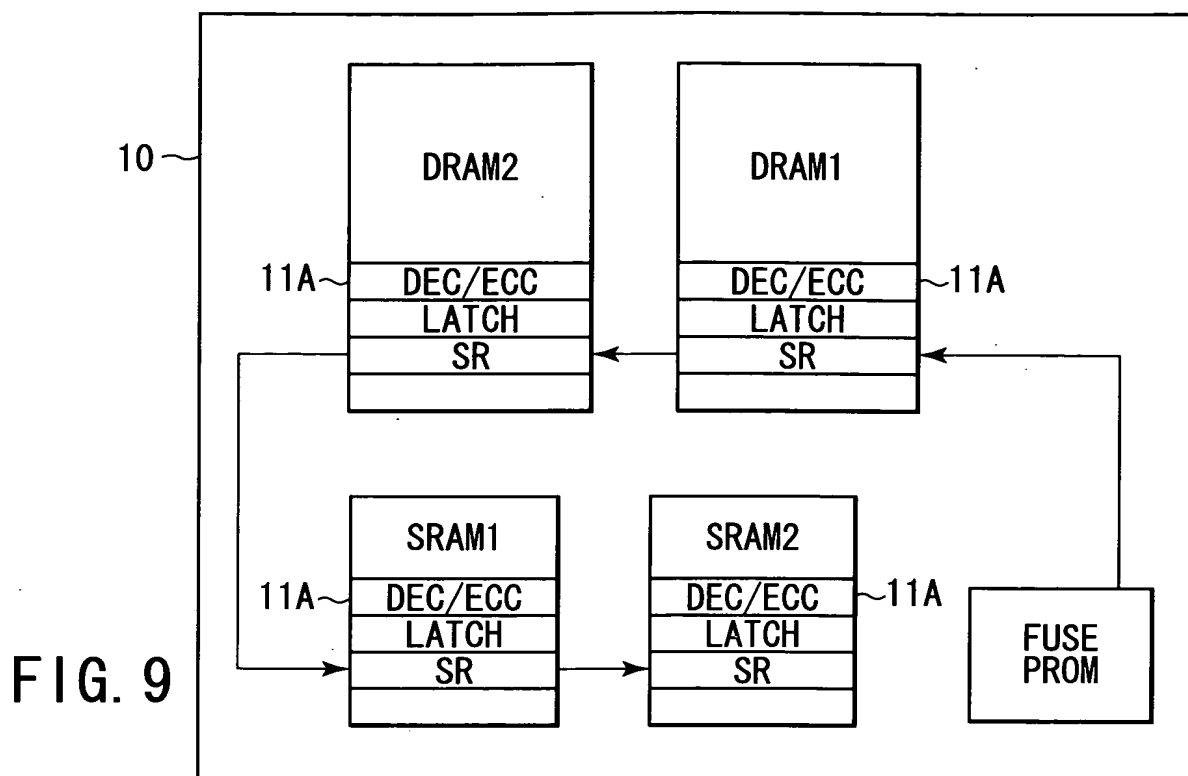


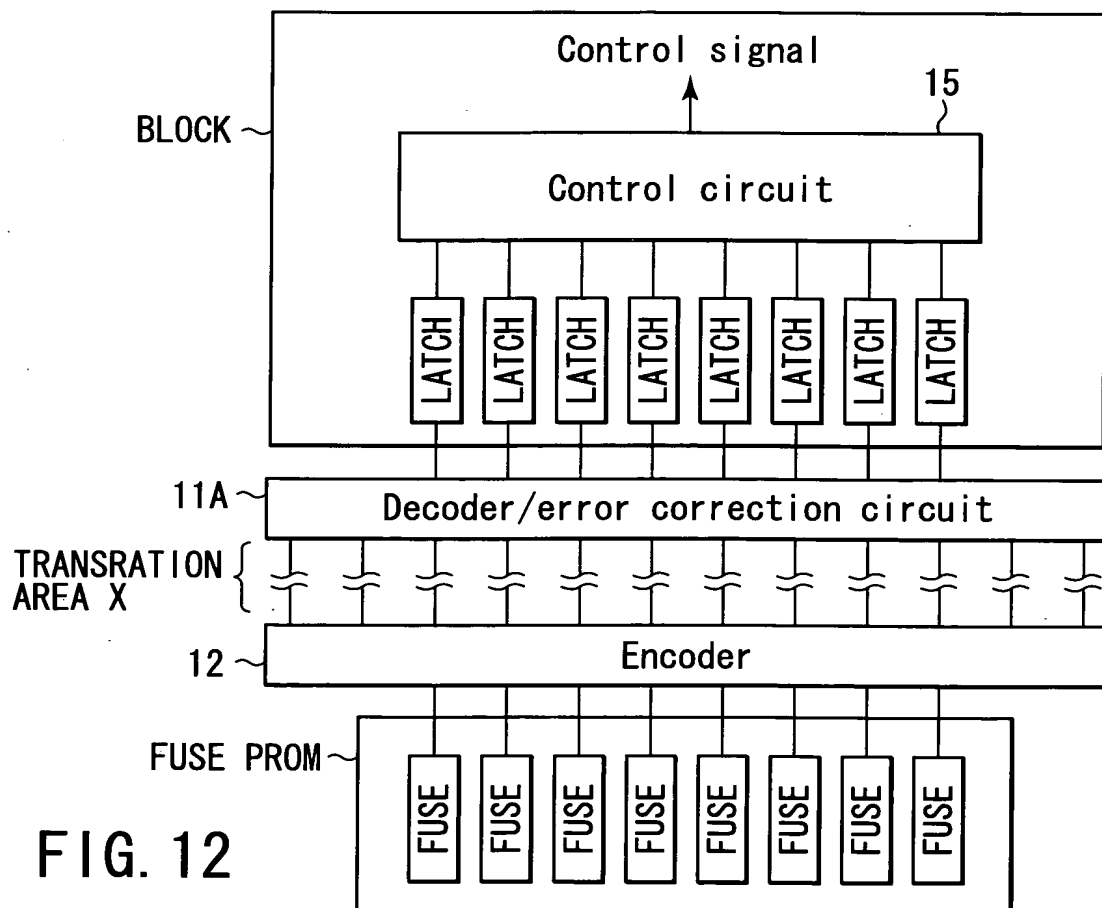
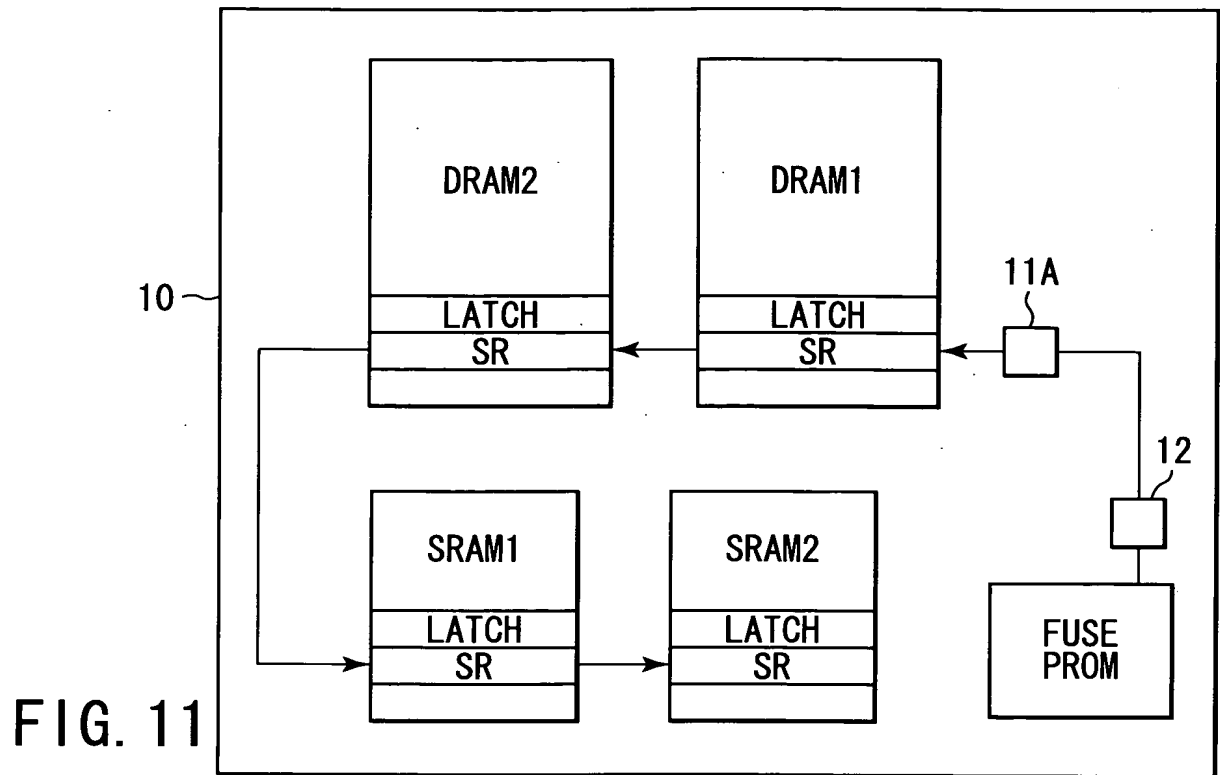


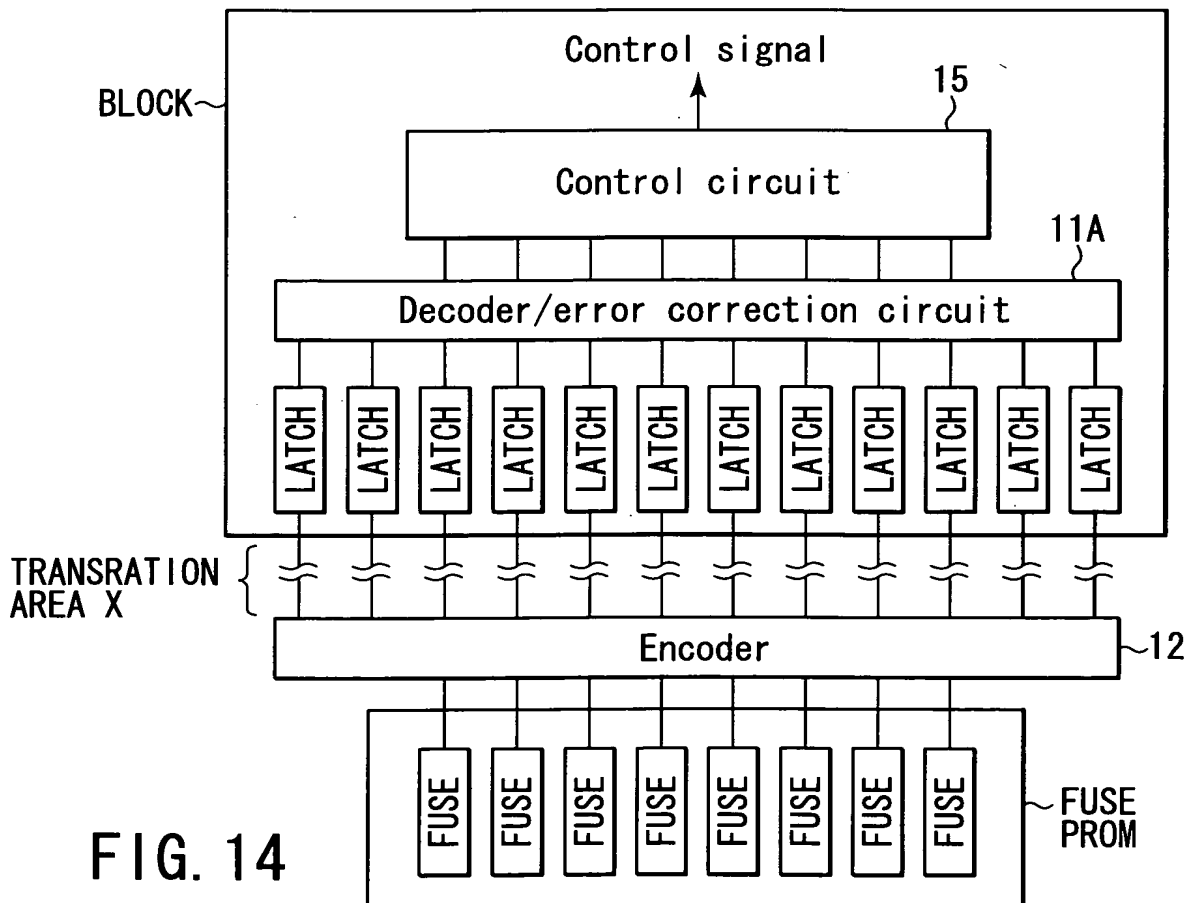
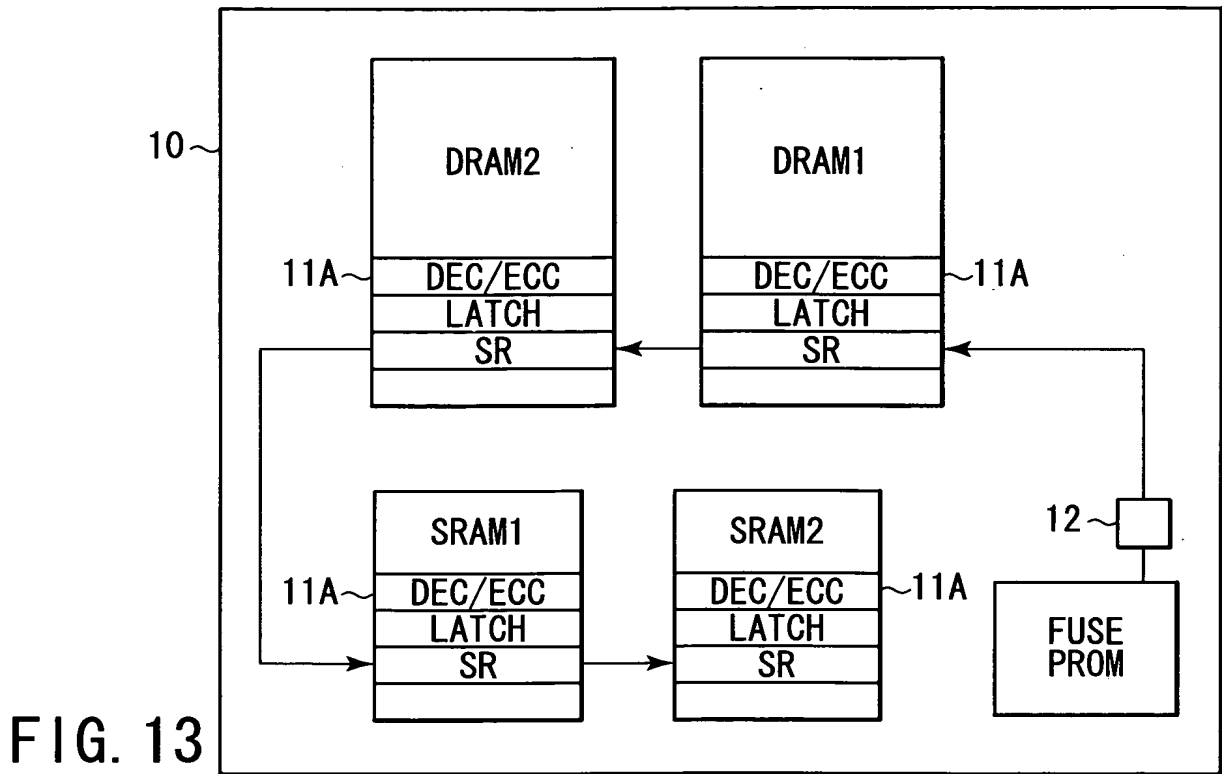
FIG. 6











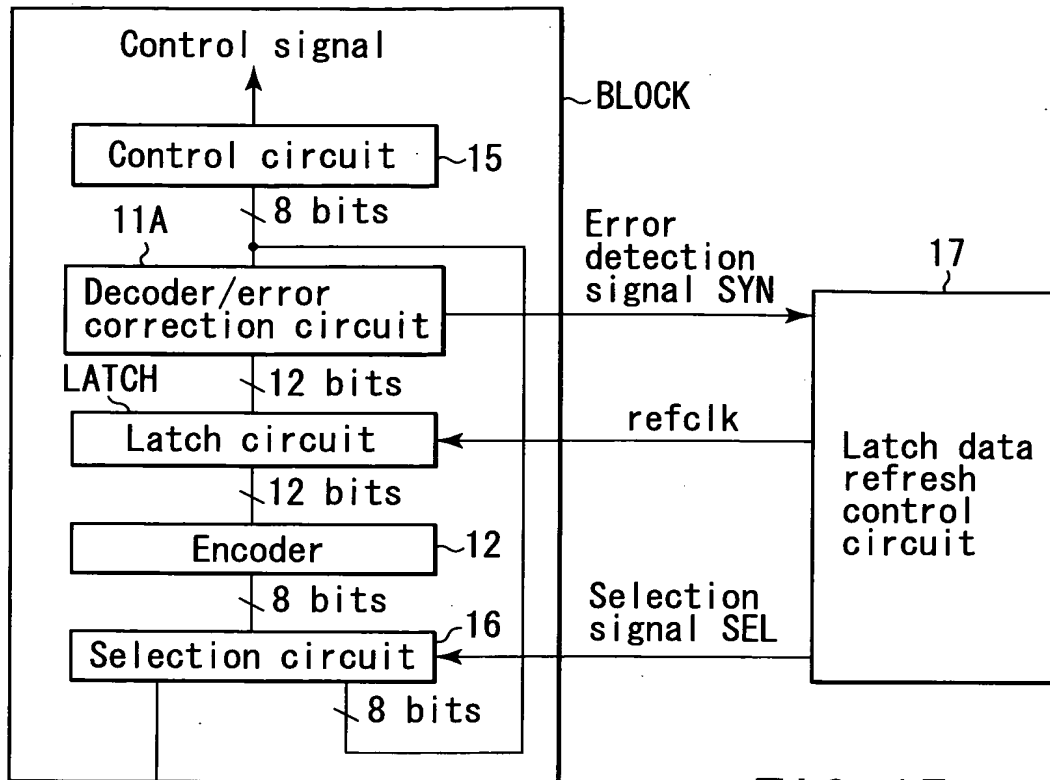


FIG. 15

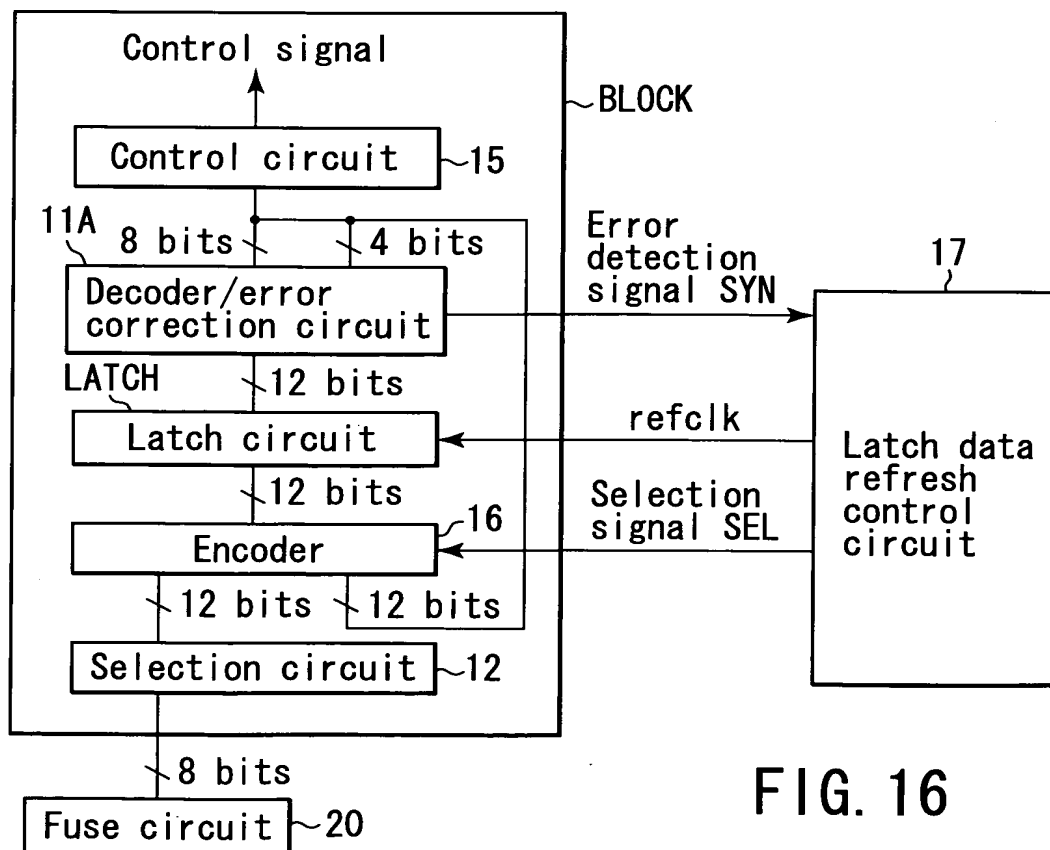


FIG. 16



FIG. 17

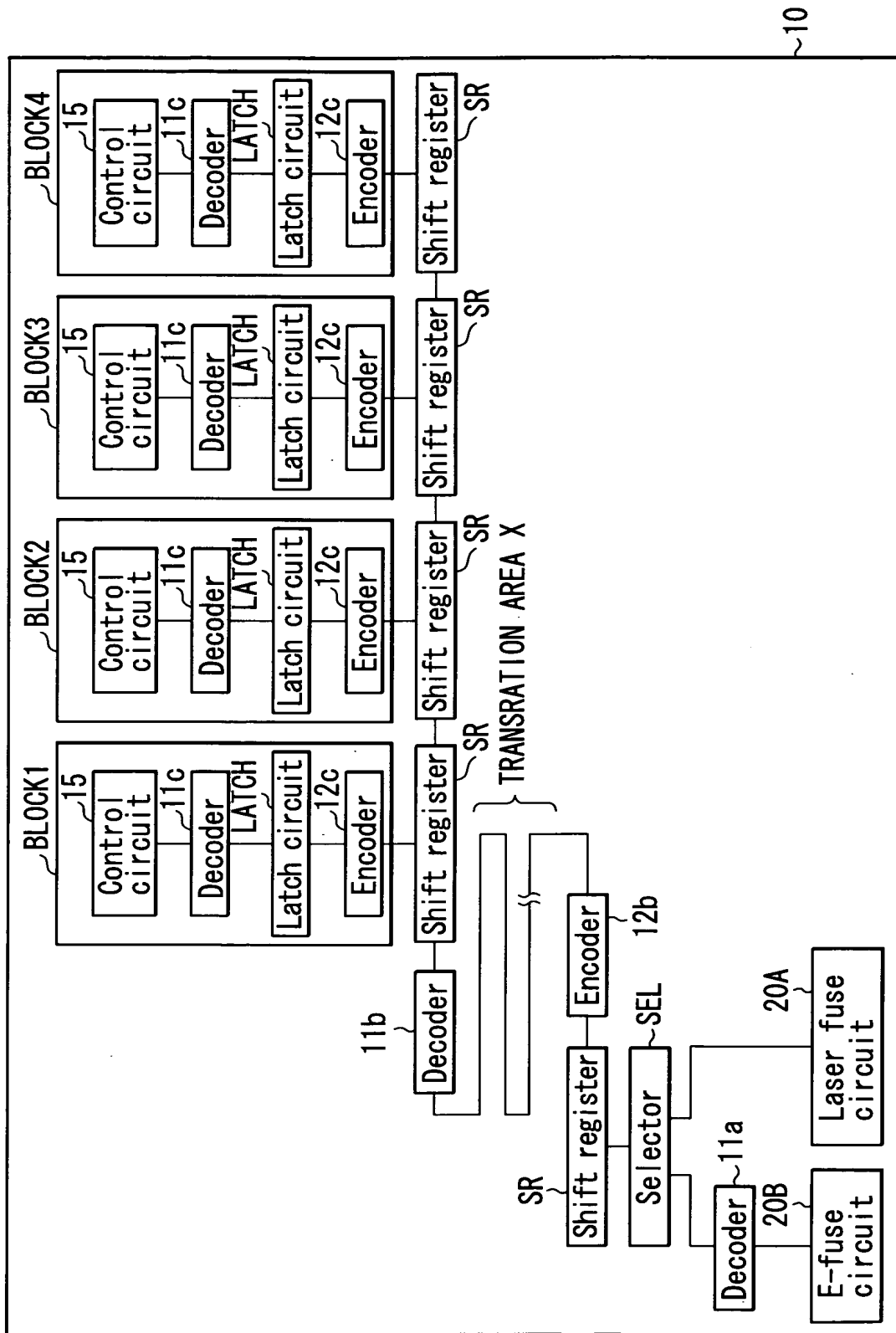


FIG. 18

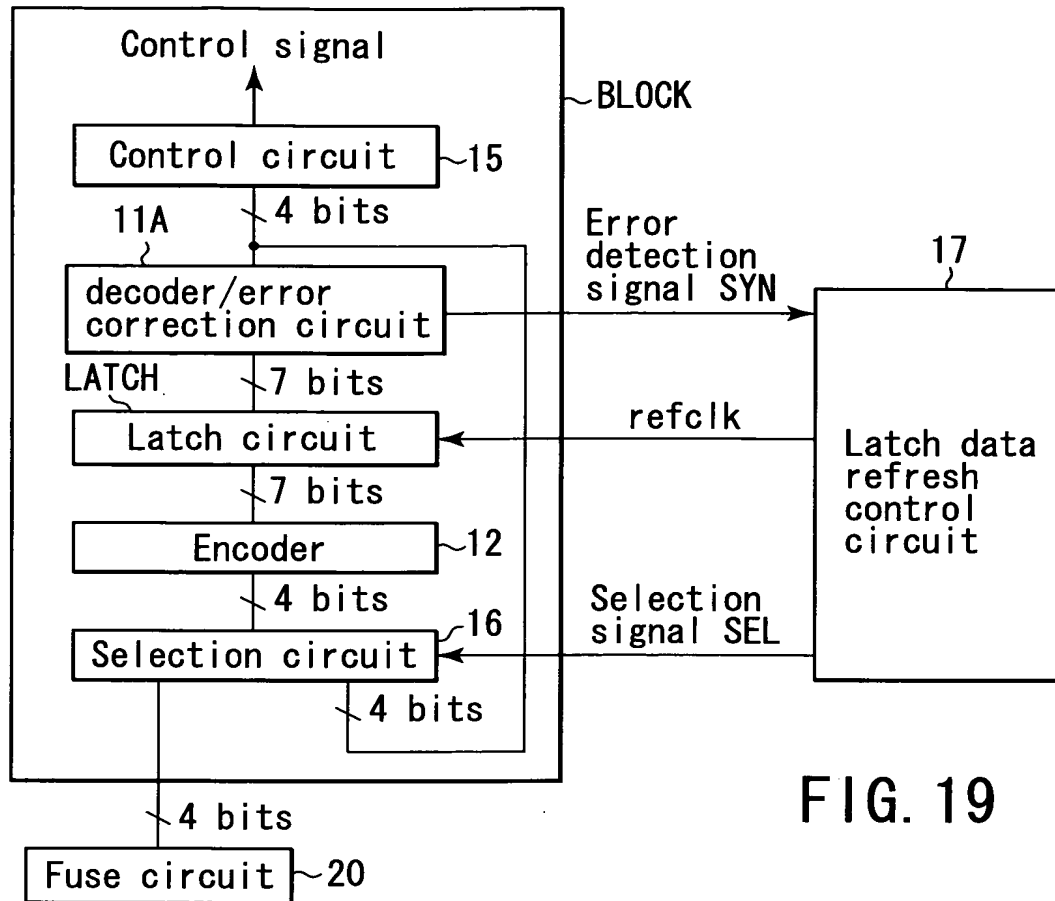


FIG. 19

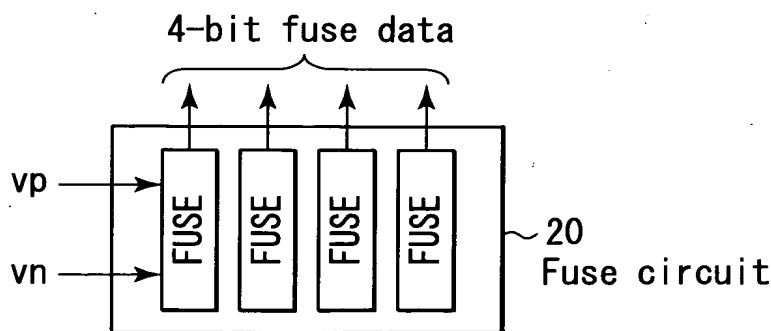


FIG. 20

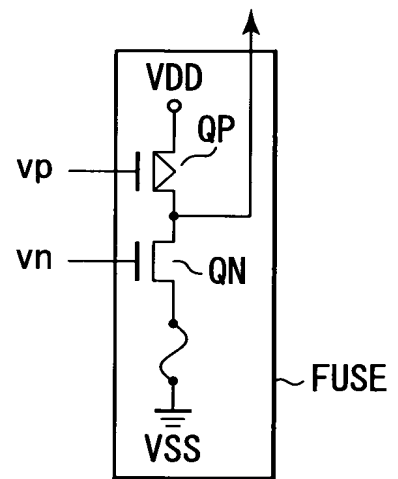


FIG. 21

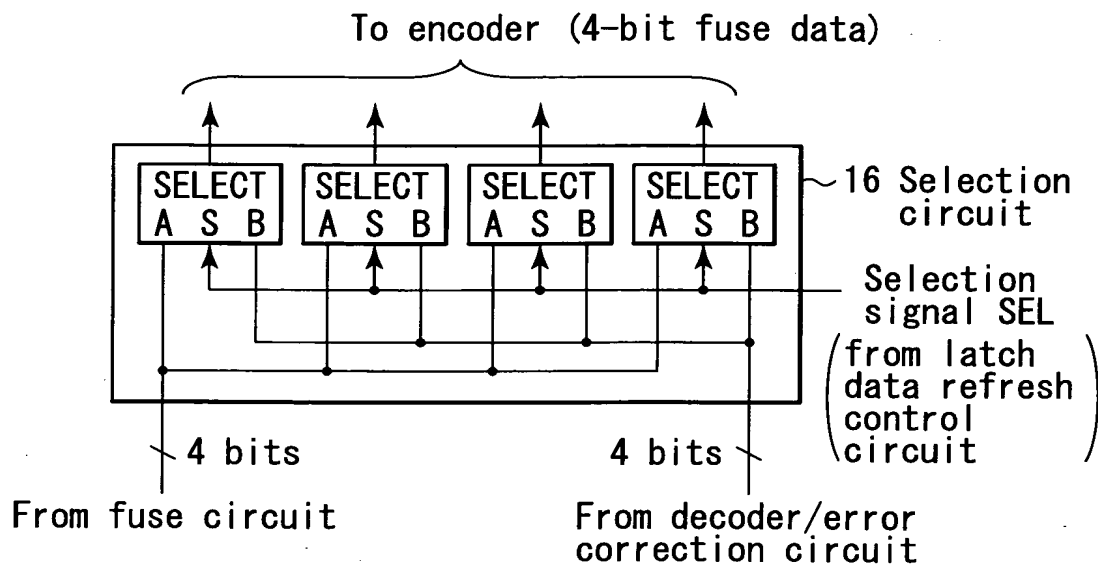
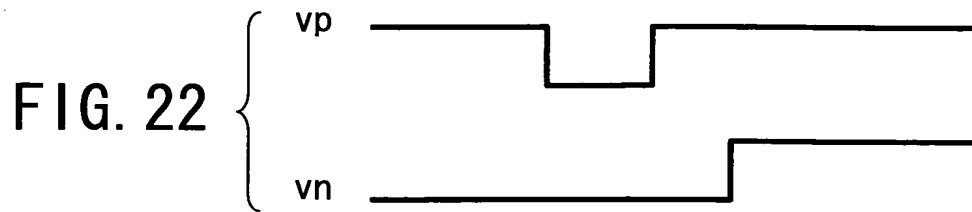
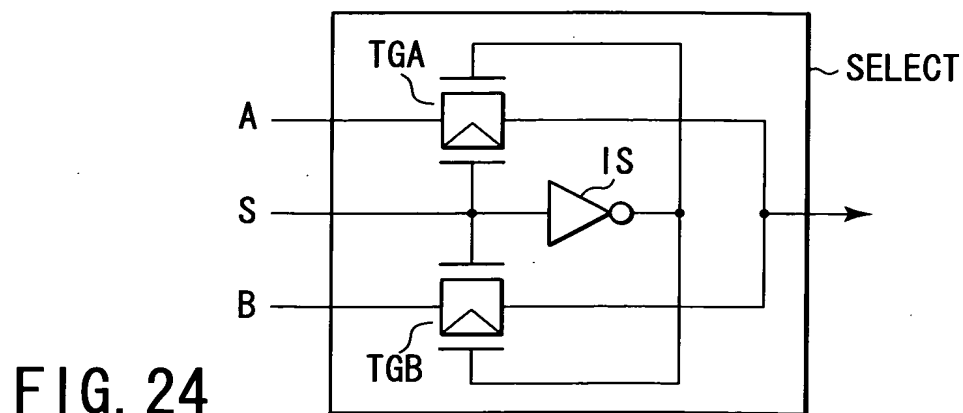
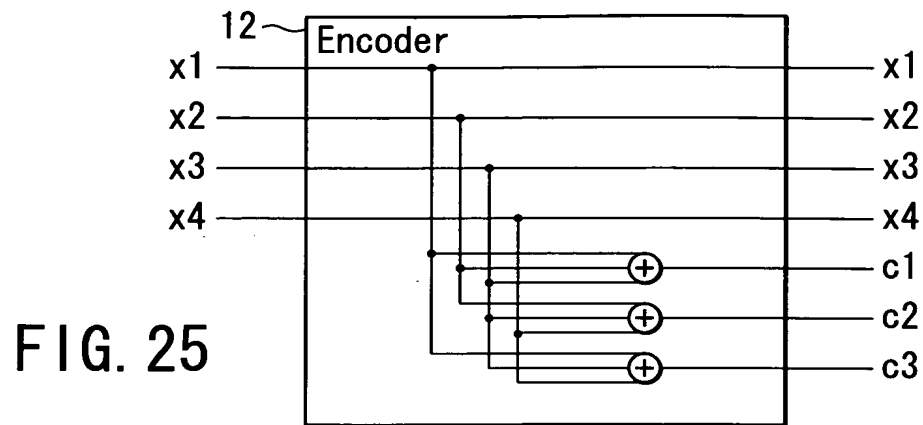
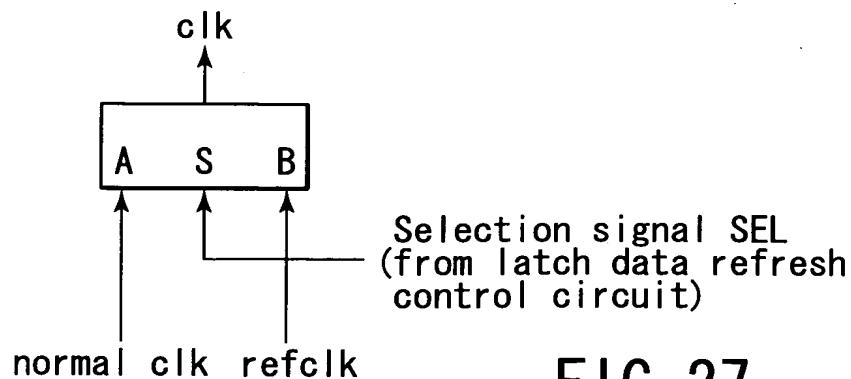
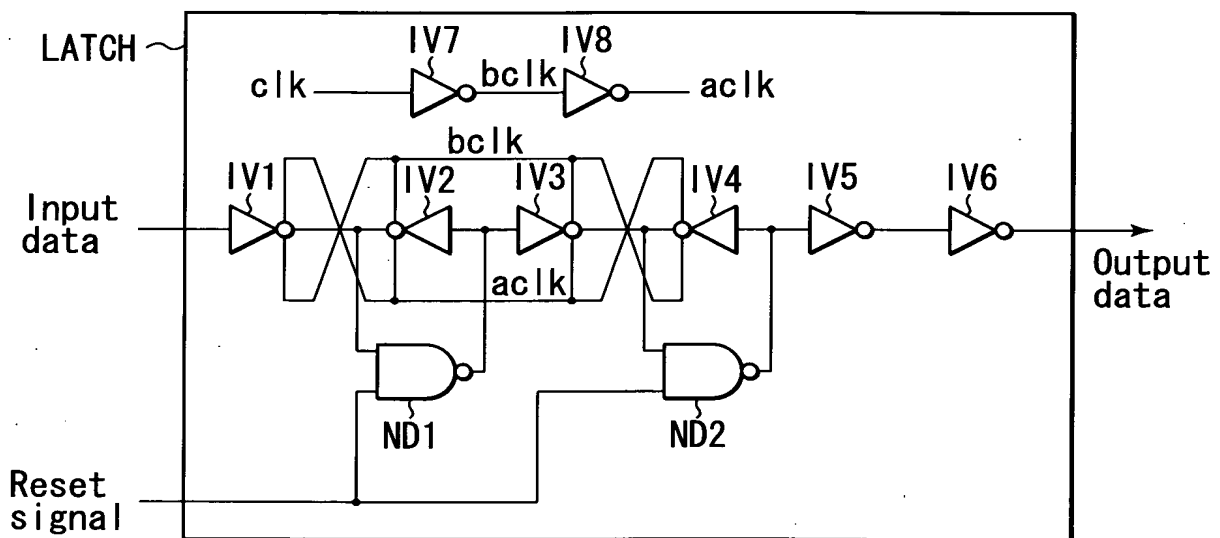


FIG. 23





⊕ : Exclusive OR (Ex-OR) circuit



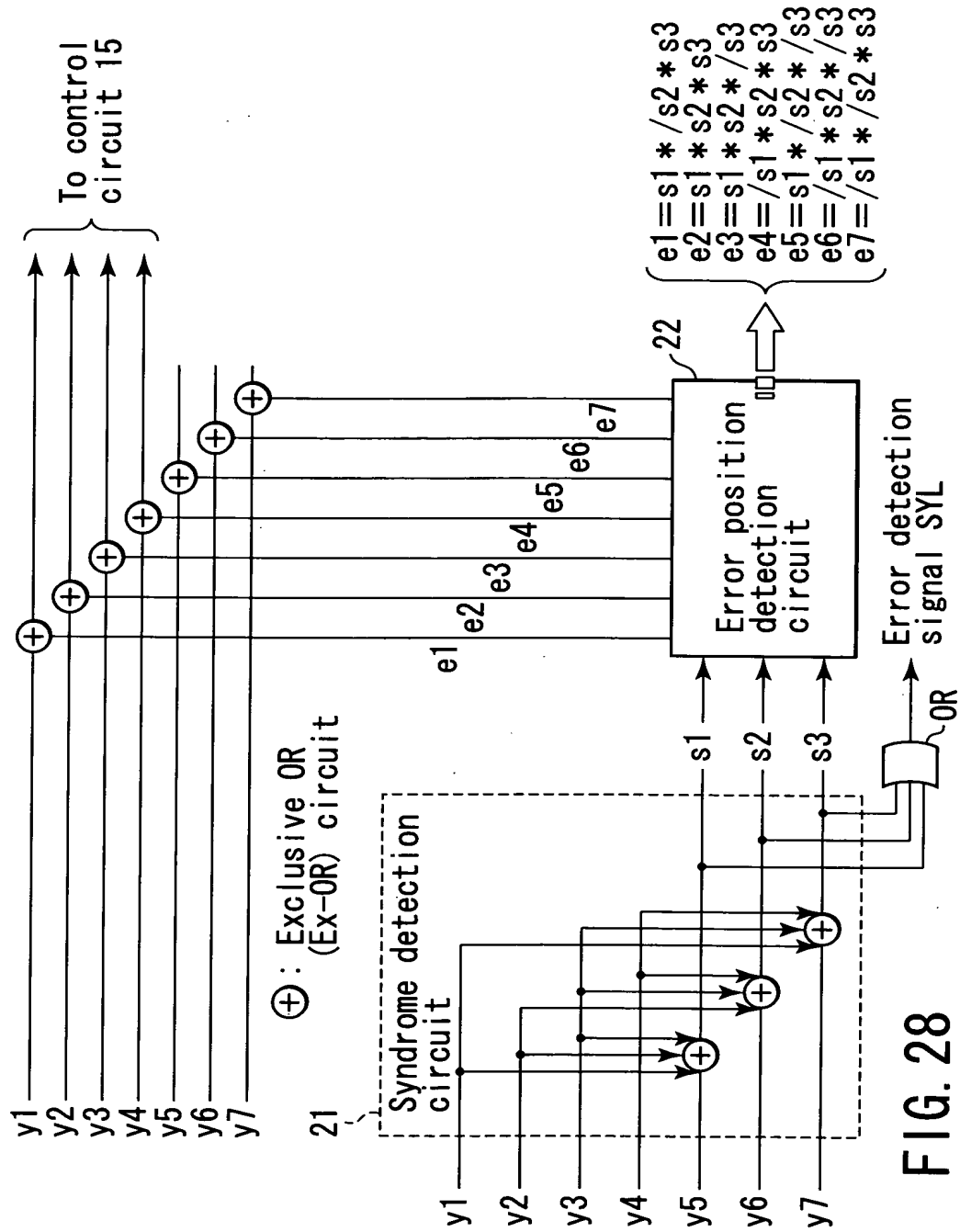


FIG. 28

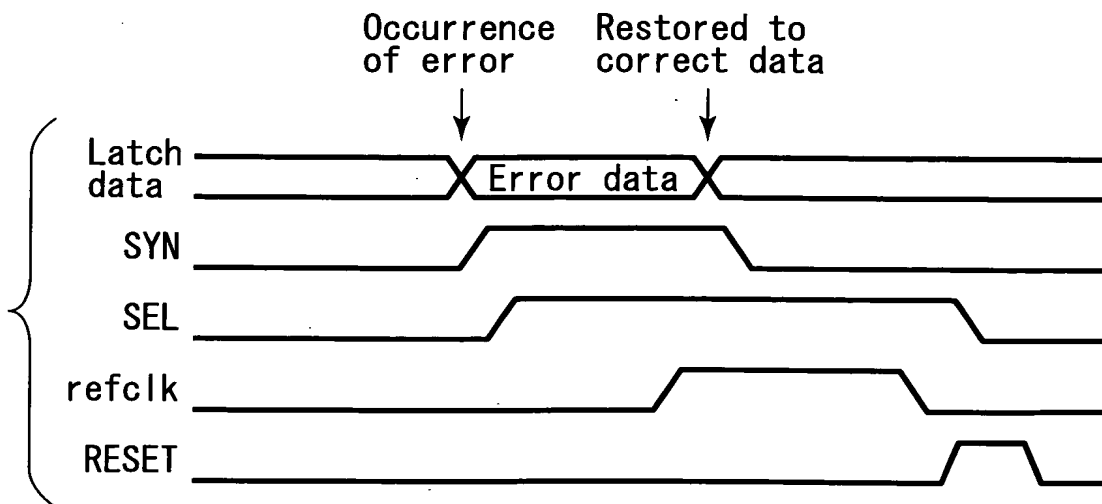
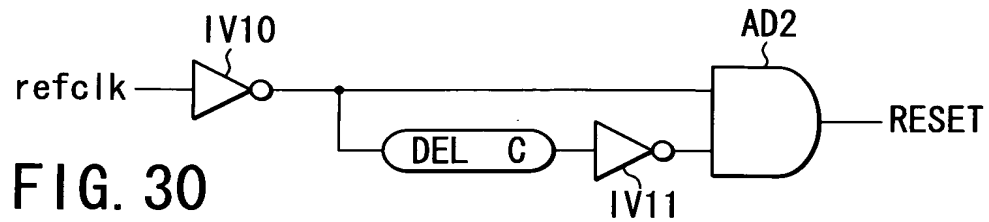
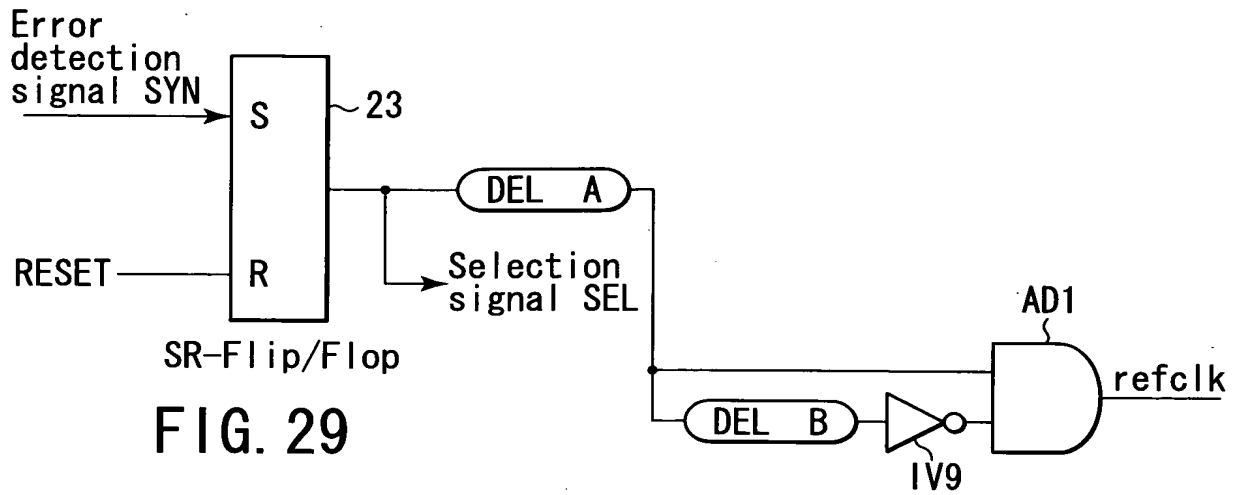


FIG. 31